

Substitute for form 1449A/PTO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	10/759489
(use as many sheets as necessary)				Filing Date	1/16/2004
				First Named Inventor	Rodney E. Hooker
				Group Art Unit	2111
				Examiner Name	Kenneth S. Sim
				Attorney Docket Number	CNTR.2233
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <small>2017-2020 v3</small>	MM-DD-YYYY		
10312		6415380	07-02-2002	Toshinori Saito	712   217

FOREIGN PATENT DOCUMENTS						
Examiner Initials <sup>a</sup>	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>d</sup>
		Country Code <sup>2</sup> - Number <sup>3</sup> - Kind Code <sup>4</sup> <small>Priority</small>				
<u>JCS/C</u>	AA	GB2307319	05-21-1997	Han Heung Kim	— ( — ) —	

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(CSK)	BA	"Dual On-Chip Instruction Cache Organization In High Speed Processors." <i>IBM Technical Disclosure Bulletin</i> , IBM Corp, New York, US, Volume 37, No. 12, December 1994, pages 213-214.	

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**Examiner  
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